

MULTIPROCESSOR CONDITIONAL
ACCESS MODULE AND METHOD FOR USING SAME

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ABSTRACT OF THE DISCLOSURE

A system and method of controlling access to a media program via a receiver communicably coupleable to a conditional access module is described. The apparatus comprises a first processor, a second processor, and an interface module, communicatively coupled to the first processor and the second processor, the interface
10 module for processing all communications with the conditional access module and externally manifesting a single virtual processor to the receiver.

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